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IP43A-A7S

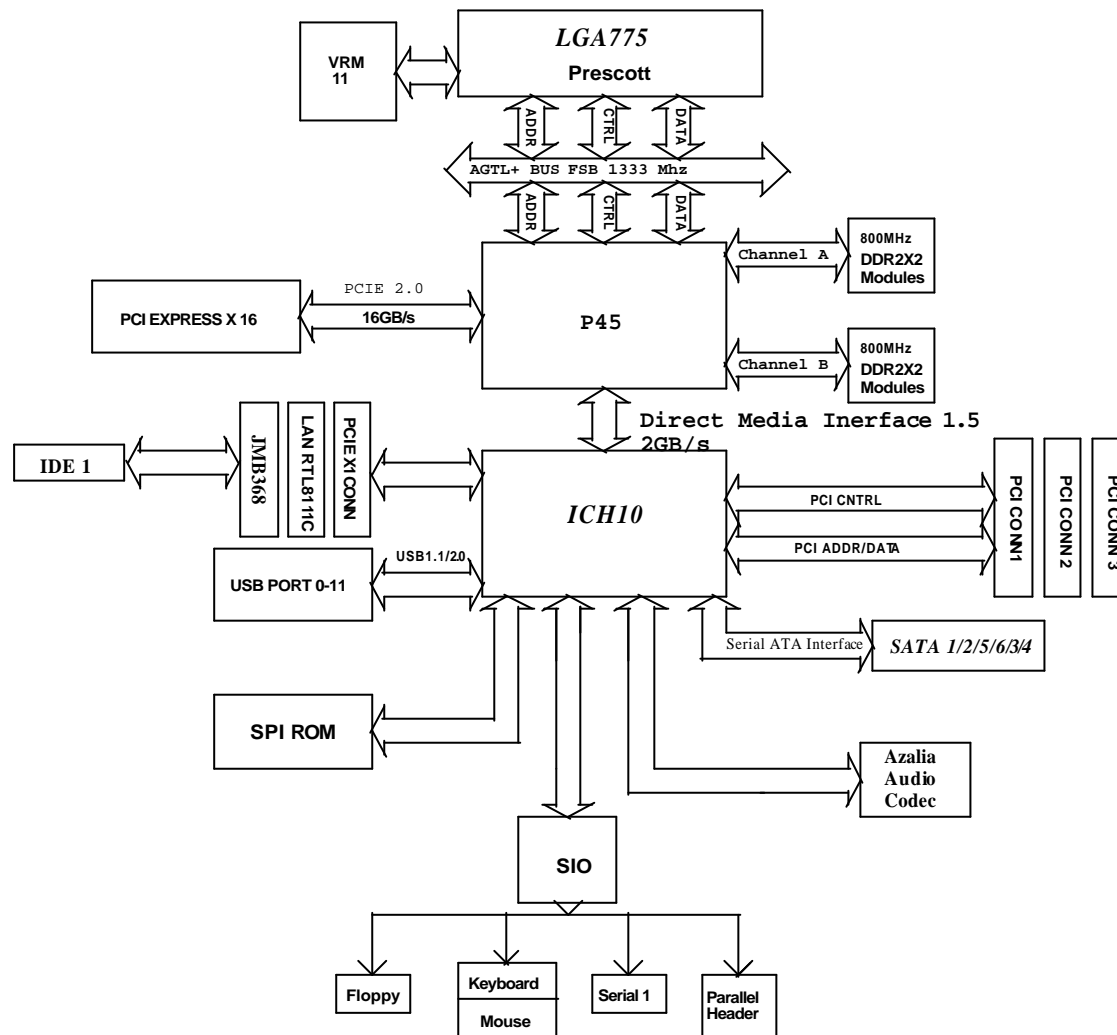
(IP43 LGA775)

Intel P45 + ICH10

VER:6.0

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COVER SHEET			
Size Custom	Document Number IP43A-A7S		Rev 6.0
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BLOCK DIAGRAM



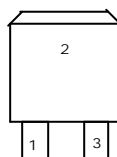
PCI Solt	IDE SEL	INT#	GNT#	REQ#	CLOCK
1	17	A	0	0	PCLK_0
2	18	B	1	1	PCLK_1
3	21	C	2	2	PCLK1_1

Chips	IDE SEL	INT#	GNT#	REQ#	CLOCK	GPIO
LAN	19	D	3	3	PCLK_3	
ICH8						
S I/O						
BIOS						
ANTI-POP						
DIMM-OV						
CPU-OV						
SATA-LED						
S I/O						
1394						

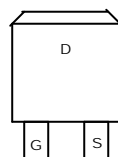
IP45A-A7S

1. CPU --- INTEL P4-775(4-Phase Power)
2. CHIPSET --- Intel P45 + ICH10
3. MEMORY --- 4 DDR2(Max. 8GB)
4. SLOTS --- PE16x1, PE1x1, PCIx3
5. IDE --- JMB368 PCIE Interface
6. CODEC --- Realtek ALC662 5.1Channel Audio
7. LAN --- Realtek RTL8111C
8. PCB Size --- ATX,305.0mmx220.0mm, 4-Layer

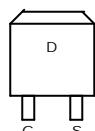
22U/25DE	5*7 mm
100U/16DE	6.3*11 mm
220U/10DE	6.3*11 mm
470U/16DE	8*11 mm
1000U/10DE	8*14 mm
1500U/16DE	10*25 mm
3300U/25DE	10*25 mm



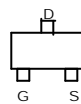
TO-263
B55QS03



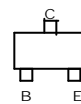
TO-263
29K3296



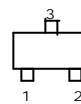
TO-252
20N03
TM3055TL-S
45N03
FDD6030L



SOT-23
2N7002
SI2303S
SI2301S



SOT-23
2N3904
2N3906
MMBT2907A



SOT-23
BAT54C
BAT54S



TO-92
LM431
78L05-D
LM432



TO-92
2N2222A
2N2097A



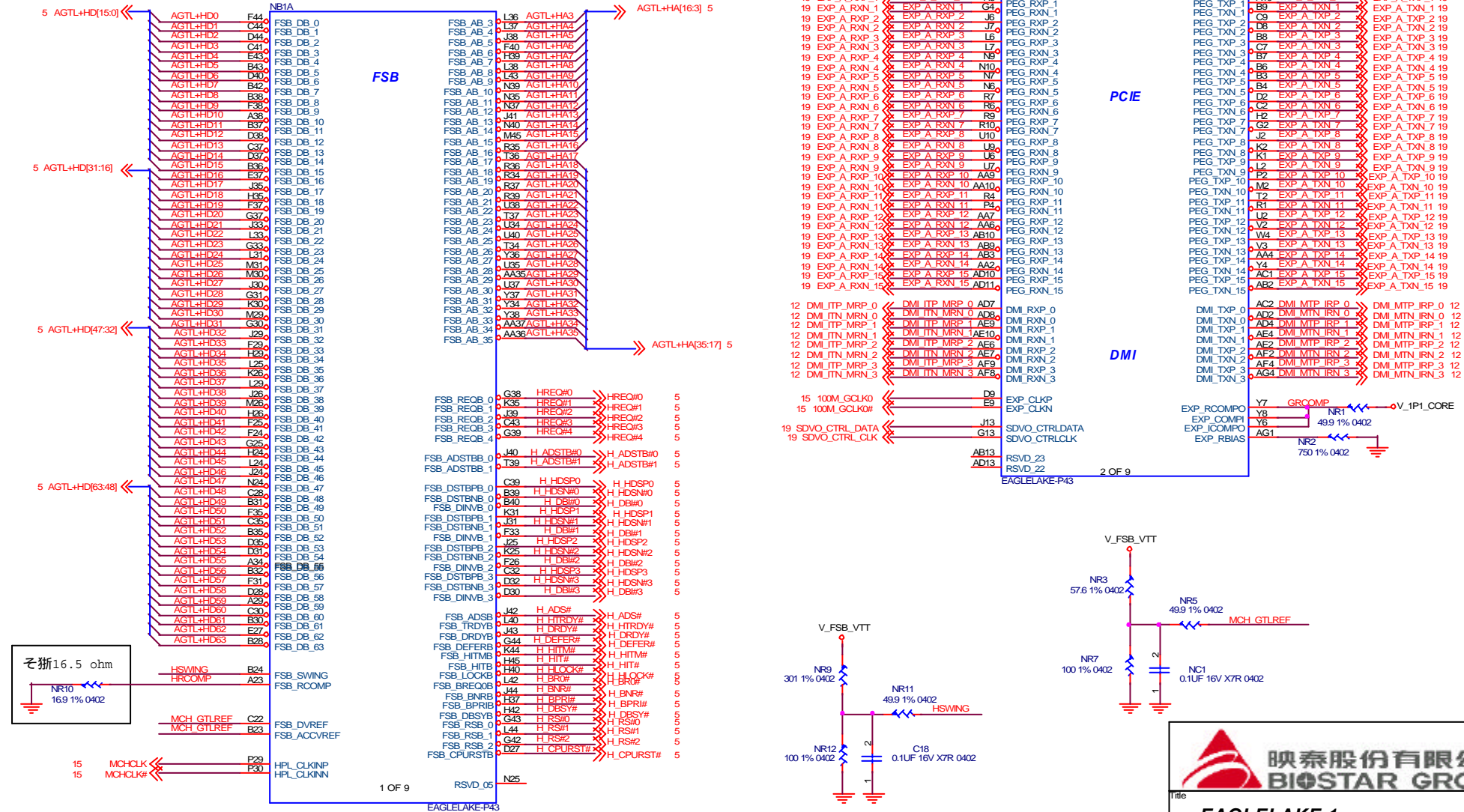
TO-92
HSD882-D

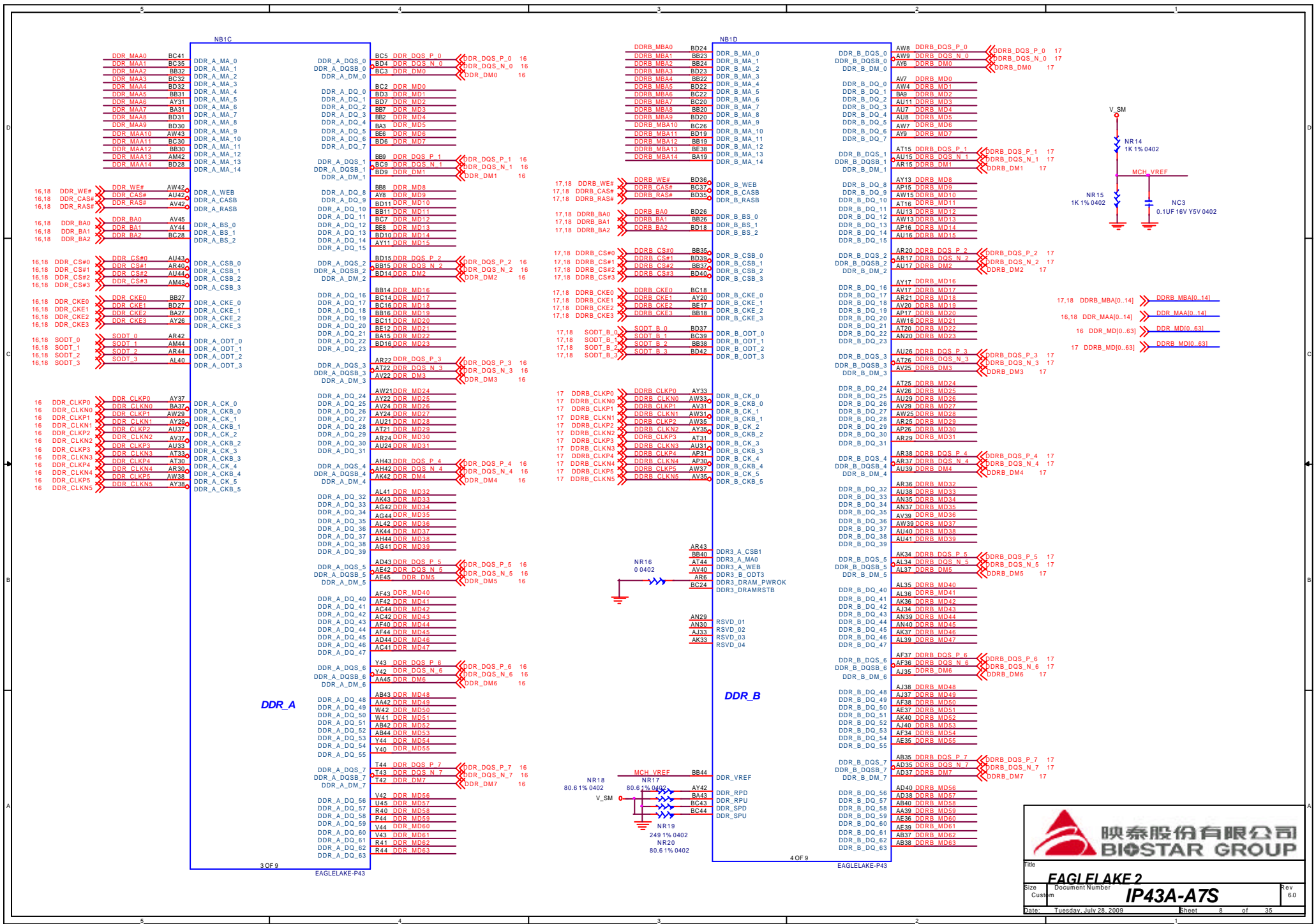


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Title		
CHANGE LIST		
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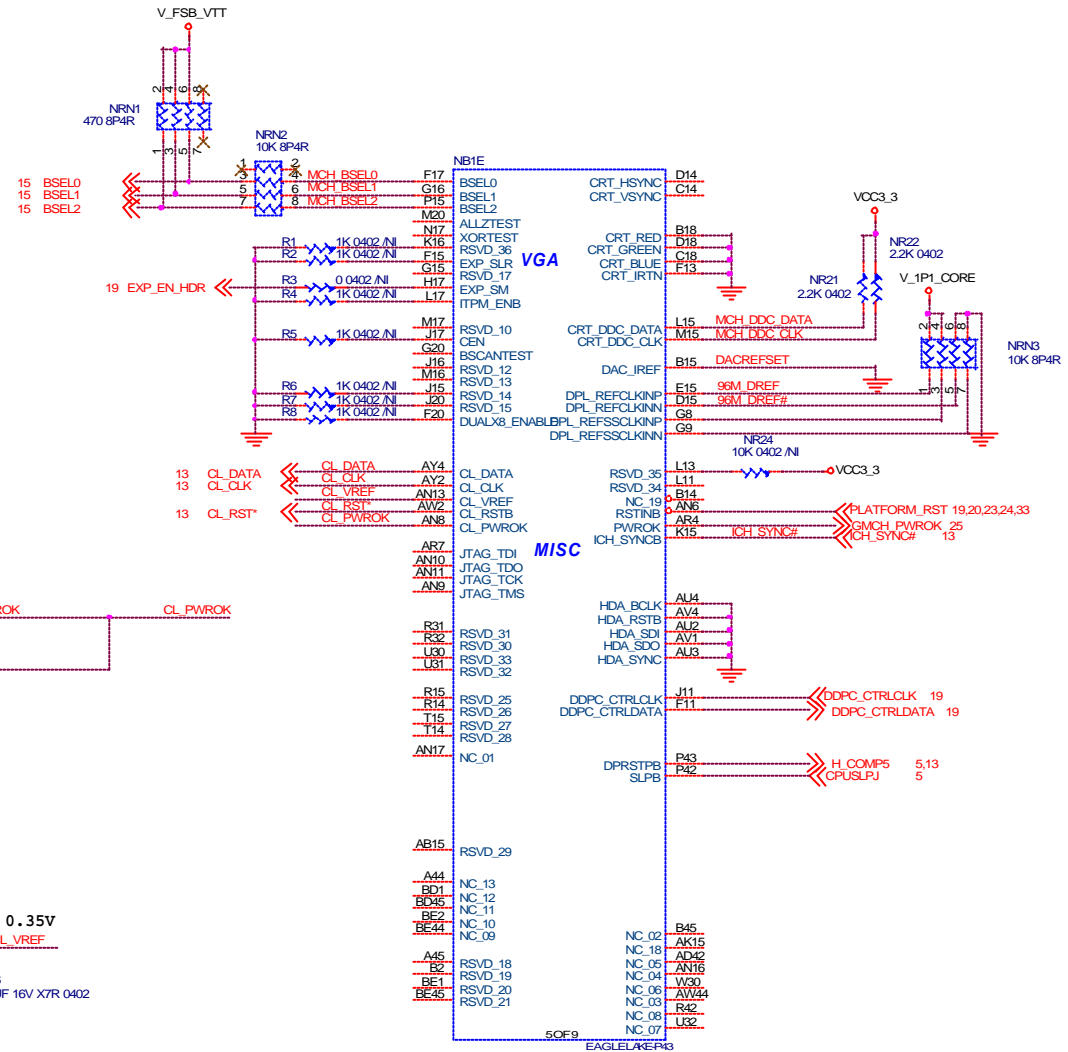
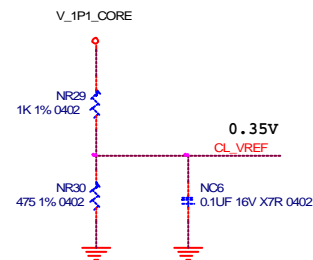




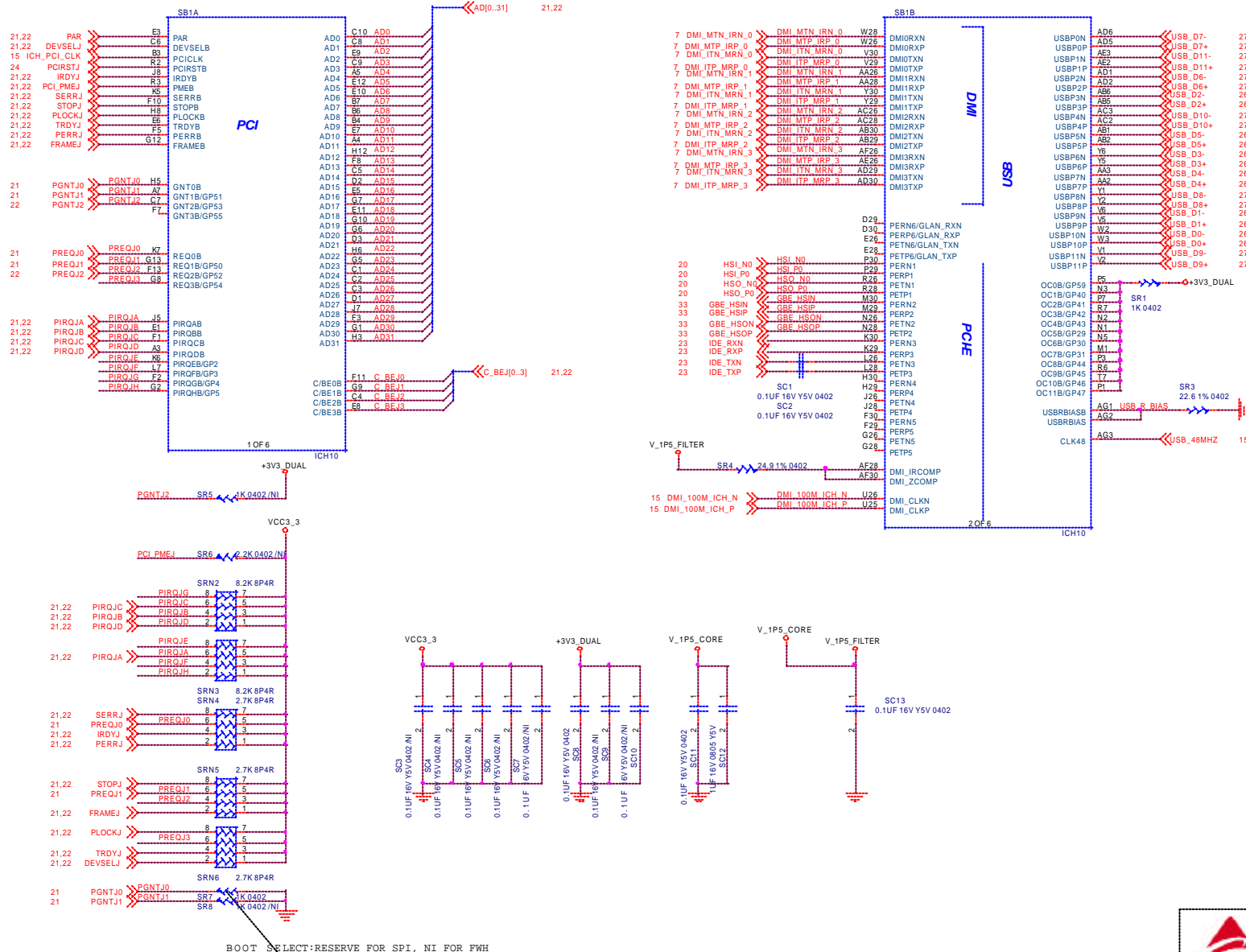


BSEL TABLE			
2	1	0	PSB FREQUENCY
0	0	0	267 MHZ (1066)
0	0	1	133 MHZ (533)
0	1	0	200 MHZ (800)
1	0	0	333 MHZ (1333)
1	1	0	400 MHZ (1600)

GMCH STRAPS TABLE		
PIN	H	L
MTYPE	DDR2	DDR3
EXP_SLR	NORMAL	RESERVE
EXP_EN	CONCURRENT	NON-CONCURRENT
TCEN	ENABLE	DISABLE

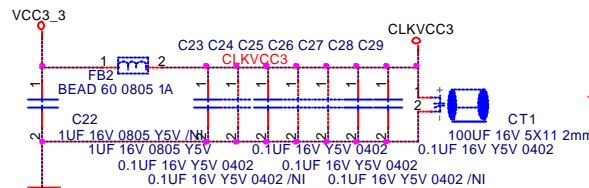


AE1	VSS_150	VSS_368	A3	AP5	VSS_119	VSS_030	AB23
AE2	VSS_080	VSS_259	L26	E3	VSS_209	VSS_120	AR10
AE3	VSS_061	VSS_260	A3	D3	VSS_029	VSS_210	E41
AE4	VSS_151	VSS_366	A3	AP3	VSS_208	VSS_211	AR11
AE5	VSS_242	VSS_367	L35	AB3	VSS_118	VSS_121	AB25
AE6	VSS_152	VSS_365	B24	AB1	VSS_029	VSS_032	AB27
AE7	VSS_062	VSS_262	L39	D6	VSS_207	VSS_122	AR13
AE8	VSS_063	VSS_263	B31	D3	VSS_117	VSS_212	AB34
AE9	VSS_361	VSS_361	L8	AP4	VSS_206	VSS_033	AR16
AE10	VSS_153	VSS_360	BC45	AB1	VSS_126	VSS_213	F16
AE11	VSS_064	VSS_360	L9	AB1	VSS_025	VSS_124	AR26
AE12	VSS_245	VSS_265	D2	AP1	VSS_205	VSS_034	AB36
AE13	VSS_154	VSS_369	D2	AB1	VSS_115	VSS_214	AB39
AE14	VSS_065	VSS_368	BD44	D3	VSS_024	VSS_035	AR33
AE15	VSS_155	VSS_368	BE33	AP2	VSS_114	VSS_125	AR3
AE16	VSS_066	VSS_267	M24	AB1	VSS_113	VSS_126	AB4
AE17	VSS_247	VSS_366	BE43	D2	VSS_203	VSS_216	F4
AE18	VSS_156	VSS_268	M44	AB1	VSS_112	VSS_217	AB6
AE19	VSS_067	VSS_269	C14	AP1	VSS_023	VSS_036	F42
AE20	VSS_157	VSS_364	N11	AB1	VSS_111	VSS_127	AR33
AE21	VSS_068	VSS_363	C45	AP1	VSS_022	VSS_128	AR35
AE22	VSS_158	VSS_271	R13	AB1	VSS_201	VSS_038	F43
AE23	VSS_249	VSS_362	N16	AP1	VSS_021	VSS_218	AB8
AE24	VSS_069	VSS_272	N26	C5	VSS_199	VSS_221	AC20
AE25	VSS_159	VSS_373	N29	AB1	VSS_109	VSS_130	C19
AE26	VSS_070	VSS_274	N30	AP1	VSS_020	VSS_220	G11
AE27	VSS_251	VSS_276	N36	AB1	VSS_197	VSS_223	AC24
AE28	VSS_071	VSS_277	N38	AP1	VSS_017	VSS_132	G26
AE29	VSS_160	VSS_278	N8	AB1	VSS_197	VSS_223	AC24
AE30	VSS_072	VSS_283	P31	AP1	VSS_107	VSS_042	AT11
AE31	VSS_161	VSS_284	R11	AB1	VSS_016	VSS_224	G29
AE32	VSS_073	VSS_285	R12	AP1	VSS_106	VSS_224	G29
AE33	VSS_252	VSS_286	R12	AB1	VSS_018	VSS_043	AC13
AE34	VSS_074	VSS_287	R19	AP1	VSS_195	VSS_134	AC45
AE35	VSS_162	VSS_288	R19	AB1	VSS_015	VSS_044	C3
AE36	VSS_075	VSS_289	R2	AP1	VSS_195	VSS_135	AT17
AE37	VSS_163	VSS_291	R30	AB1	VSS_104	VSS_045	AC5
AE38	VSS_076	VSS_292	R38	AP1	VSS_014	VSS_226	G35
AE39	VSS_164	VSS_293	R45	AB1	VSS_013	VSS_136	AD12
AE40	VSS_077	VSS_294	R8	AP1	VSS_103	VSS_046	H1
AE41	VSS_165	VSS_295	T10	AB1	VSS_012	VSS_047	AT29
AE42	VSS_078	VSS_296	T11	AP1	VSS_192	VSS_228	H11
AE43	VSS_166	VSS_297	T11	AB1	VSS_191	VSS_138	H13
AE44	VSS_079	VSS_298	T12	AP1	VSS_101	VSS_229	AD21
AE45	VSS_167	VSS_299	T12	AB1	VSS_010	VSS_139	A135
AE46	VSS_080	VSS_300	T17	AP1	VSS_190	VSS_230	AD25
AE47	VSS_168	VSS_301	T17	AB1	VSS_009	VSS_050	H16
AE48	VSS_081	VSS_302	T20	AP1	VSS_189	VSS_140	AU20
AE49	VSS_169	VSS_303	T20	AB1	VSS_008	VSS_232	H20
AE50	VSS_082	VSS_305	T30	AP1	VSS_187	VSS_141	AU22
AE51	VSS_170	VSS_306	T32	AB1	VSS_007	VSS_051	AD25
AE52	VSS_083	VSS_308	T33	AP1	VSS_186	VSS_143	AU35
AE53	VSS_171	VSS_309	T35	AB1	VSS_005	VSS_144	H31
AE54	VSS_084	VSS_311	T40	AP1	VSS_183	VSS_055	AD39
AE55	VSS_172	VSS_312	T6	AB1	VSS_004	VSS_145	H33
AE56	VSS_085	VSS_314	T7	AP1	VSS_002	VSS_236	AD6
AE57	VSS_173	VSS_316	T9	AB1	VSS_181	VSS_057	AU9
AE58	VSS_086	VSS_317	W2	AP1	VSS_094	VSS_238	AV11
AE59	VSS_174	VSS_322	W20	AB1	VSS_092	VSS_238	AV11
AE60	VSS_087	VSS_324	W22	AP1	VSS_091	VSS_239	H6
AE61	VSS_175	VSS_325	W44	AB1	VSS_091	VSS_239	H6
AE62	VSS_088	VSS_326	W44	AP1	VSS_091	VSS_239	H6
AE63	VSS_176	VSS_327	W45	AB1	VSS_091	VSS_239	H6
AE64	VSS_089	VSS_328	W5	AP1	VSS_091	VSS_239	H6
AE65	VSS_177	VSS_329	Y10	AB1	VSS_091	VSS_239	H6
AE66	VSS_090	VSS_330	Y12	AP1	VSS_091	VSS_239	H6
AE67	VSS_178	VSS_331	Y21	AB1	VSS_091	VSS_239	H6
AE68	VSS_091	VSS_332	Y21	AP1	VSS_091	VSS_239	H6
AE69	VSS_179	VSS_333	Y25	AB1	VSS_091	VSS_239	H6
AE70	VSS_092	VSS_334	Y27	AP1	VSS_091	VSS_239	H6
AE71	VSS_180	VSS_335	Y35	AB1	VSS_091	VSS_239	H6
AE72	VSS_093	VSS_336	Y9	AP1	VSS_091	VSS_239	H6
AE73	VSS_181	VSS_337	Y9	AB1	VSS_091	VSS_239	H6
AE74	VSS_094	VSS_338	Y9	AP1	VSS_091	VSS_239	H6
AE75	VSS_182	VSS_339	Y9	AB1	VSS_091	VSS_239	H6
AE76	VSS_095	VSS_340	Y9	AP1	VSS_091	VSS_239	H6
AE77	VSS_183	VSS_341	Y9	AB1	VSS_091	VSS_239	H6
AE78	VSS_096	VSS_342	Y9	AP1	VSS_091	VSS_239	H6
AE79	VSS_184	VSS_343	Y9	AB1	VSS_091	VSS_239	H6
AE80	VSS_097	VSS_344	Y9	AP1	VSS_091	VSS_239	H6
AE81	VSS_185	VSS_345	Y9	AB1	VSS_091	VSS_239	H6
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AE86	VSS_100	VSS_350	Y9	AP1	VSS_091	VSS_239	H6
AE87	VSS_010	VSS_351	Y9	AB1	VSS_091	VSS_239	H6
AE88	VSS_190	VSS_352	Y9	AP1	VSS_091	VSS_239	H6
AE89	VSS_009	VSS_353	Y9	AB1	VSS_091	VSS_239	H6
AE90	VSS_188	VSS_354	Y9	AP1	VSS_091	VSS_239	H6
AE91	VSS_008	VSS_355	Y9	AB1	VSS_091	VSS_239	H6
AE92	VSS_187	VSS_356	Y9	AP1	VSS_091	VSS_239	H6
AE93	VSS_007	VSS_357	Y9	AB1	VSS_091	VSS_239	H6
AE94	VSS_186	VSS_358	Y9	AP1	VSS_091	VSS_239	H6
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AE99	VSS_004	VSS_363	Y9	AB1	VSS_091	VSS_239	H6
AE100	VSS_183	VSS_364	Y9	AP1	VSS_091	VSS_239	H6
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AE103	VSS_002	VSS_367	Y9	AB1	VSS_091	VSS_239	H6
AE104	VSS_181	VSS_368	Y9	AP1	VSS_091	VSS_239	H6
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AE111	VSS_000	VSS_375	Y9	AB1	VSS_091	VSS_239	H6
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AE115	VSS_000	VSS_379	Y9	AB1	VSS_091	VSS_239	H6
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AE122	VSS_172	VSS_386	Y9	AP1	VSS_091	VSS_239	H6
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AE126	VSS_170	VSS_390	Y9	AP1	VSS_091	VSS_239	H6
AE127	VSS_000	VSS_391	Y9	AB1	VSS_091	VSS_239	H6
AE128	VSS_169	VSS_392	Y9	AP1	VSS_091	VSS_239	H6
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AE132	VSS_167	VSS_396	Y9	AP1	VSS_091	VSS_239	H6
AE133	VSS_000	VSS_397	Y9	AB1	VSS_091	VSS_239	H6
AE134	VSS_166	VSS_398	Y9	AP1	VSS_091	VSS_239	H6
AE135	VSS_000	VSS_399	Y9	AB1	VSS_091	VSS_239	H6
AE136	VSS_165	VSS_400	Y9	AP1	VSS_091	VSS_239	H6
AE137	VSS_000	VSS_401	Y9	AB1	VSS_091	VSS_239	H6
AE138	VSS_164	VSS_402	Y9	AP1	VSS_091	VSS_239	H6
AE139	VSS_000	VSS_403	Y9	AB1	VSS_091	VSS_239	H6
AE140	VSS_163	VSS_404	Y9	AP1	VSS_091	VSS_239	H6
AE141	VSS_000	VSS_405	Y9	AB1	VSS_091	VSS_239	H6
AE142	VSS_162	VSS_406	Y9	AP1	VSS_091	VSS_239	H6
AE143	VSS_000	VSS_407	Y9	AB1	VSS_091	VSS_239	H6
AE144	VSS_161	VSS_408	Y9	AP1	VSS_091	VSS_239	H6
AE145	VSS_000	VSS_409	Y9	AB1	VSS_091	VSS_239	H6
AE146	VSS_160	VSS_410	Y9	AP1	VSS_091	VSS_239	H6
AE147	VSS_000	VSS_411	Y9	AB1	VSS_091	VSS_239	H6
AE148	VSS_159	VSS_412	Y9	AP1	VSS_091	VSS_239	H6
AE149	VSS_000	VSS_413	Y9	AB1	VSS_091	VSS_239	H6
AE150	VSS_158	VSS_414	Y9	AP1	VSS_091	VSS_239	H6
AE151	VSS_000	VSS_415	Y9	AB1	VSS_091	VSS_239	H6
AE152	VSS_157	VSS_416	Y9	AP1	VSS_091	VSS_239	H6
AE153	VSS_000	VSS_417	Y9	AB1	VSS_091	VSS_239	H6
AE154	VSS_156	VSS_418	Y9	AP1	VSS_091	VSS_239	H6
AE155	VSS_000	VSS_419	Y9	AB1	VSS_091	VSS_239	H6
AE156	VSS_155	VSS_420	Y9	AP1	VSS_091	VSS_239	H6
AE157	VSS_000	VSS_421	Y9	AB1	VSS_091	VSS_239	H6
AE158	VSS_154	VSS_422	Y9	AP1	VSS_091	VSS_239	H6
AE159	VSS_000	VSS_423	Y9	AB1	VSS_091	VSS_239	H6
AE160	VSS_153	VSS_424	Y9	AP1	VSS_091	VSS_239	H6
AE161	VSS_000	VSS_425	Y9	AB1	VSS_091	VSS_239	H6
AE162	VSS_152	VSS_426	Y9	AP1	VSS_091	VSS_239	H6
AE163	VSS_000	VSS_427	Y9	AB1	VSS_091	VSS_239	H6
AE164	VSS_151	VSS_428	Y9	AP1	VSS_091	VSS_239	H6
AE165	VSS_000	VSS_429</					

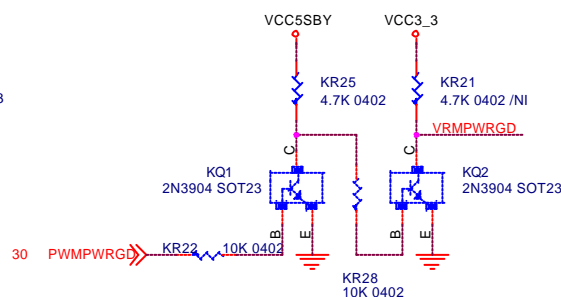
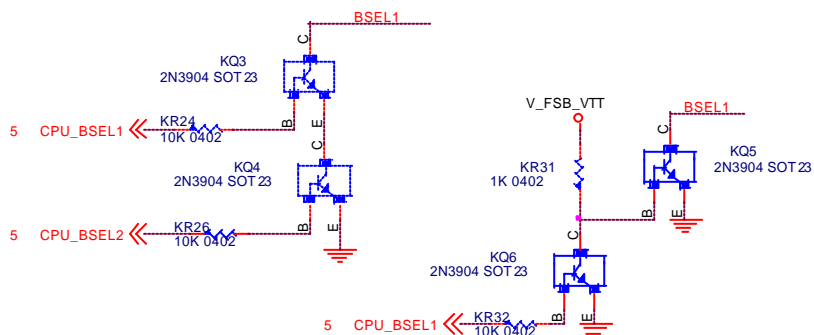
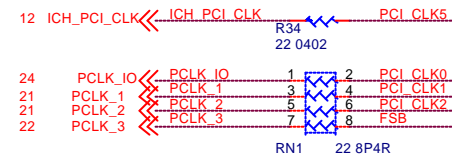
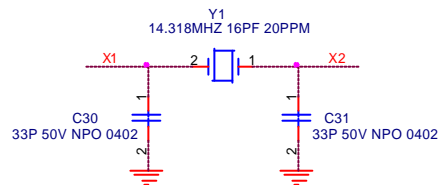
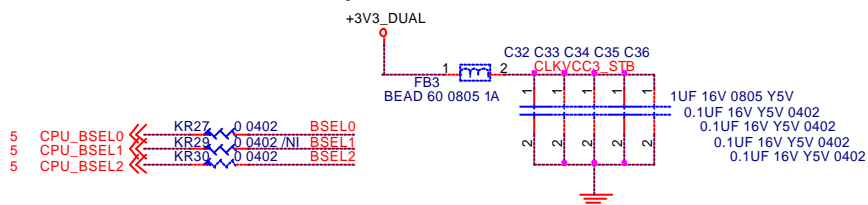
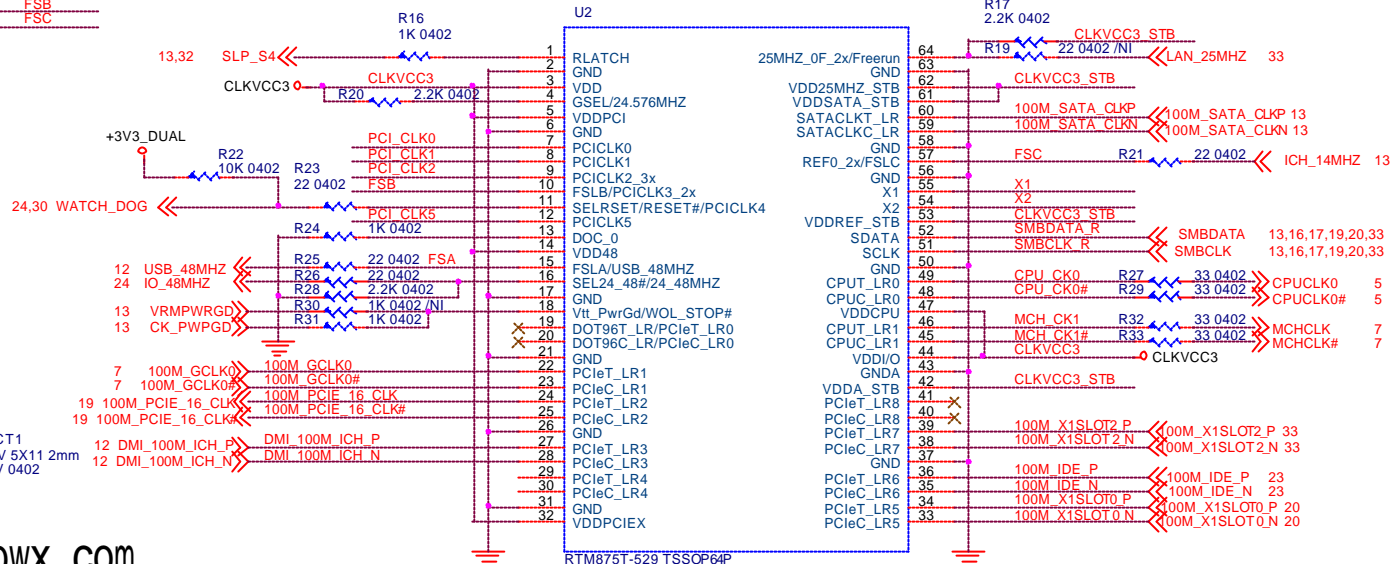


BOOT SELECT:RESERVE FOR SPI, NI FOR FW

BSEL2	BSEL1	BSEL0	
0	0	1	133MHz
0	1	0	200MHz
0	0	0	266MHz
1	0	0	333MHz

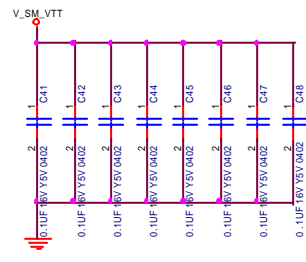
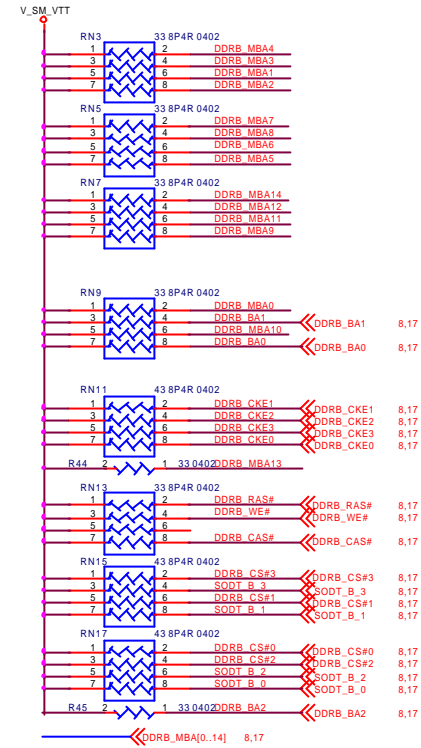
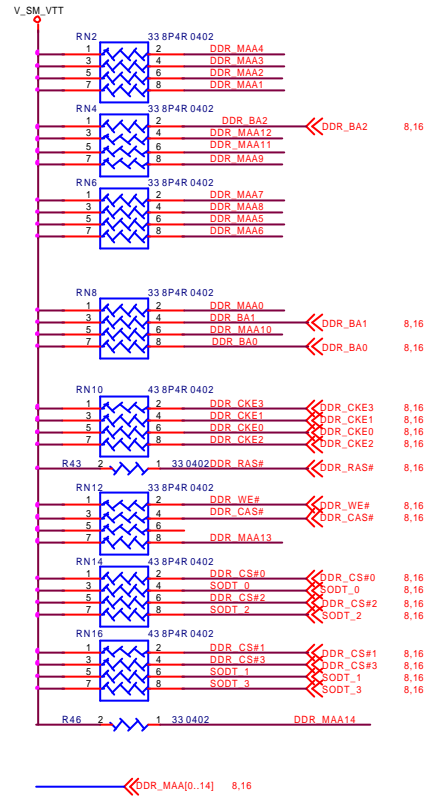


起点主板维修网 www.qdzbwx.com

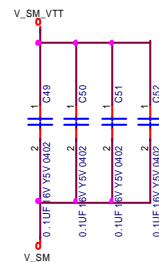


起点主板维修网 www.qdzbwx.com





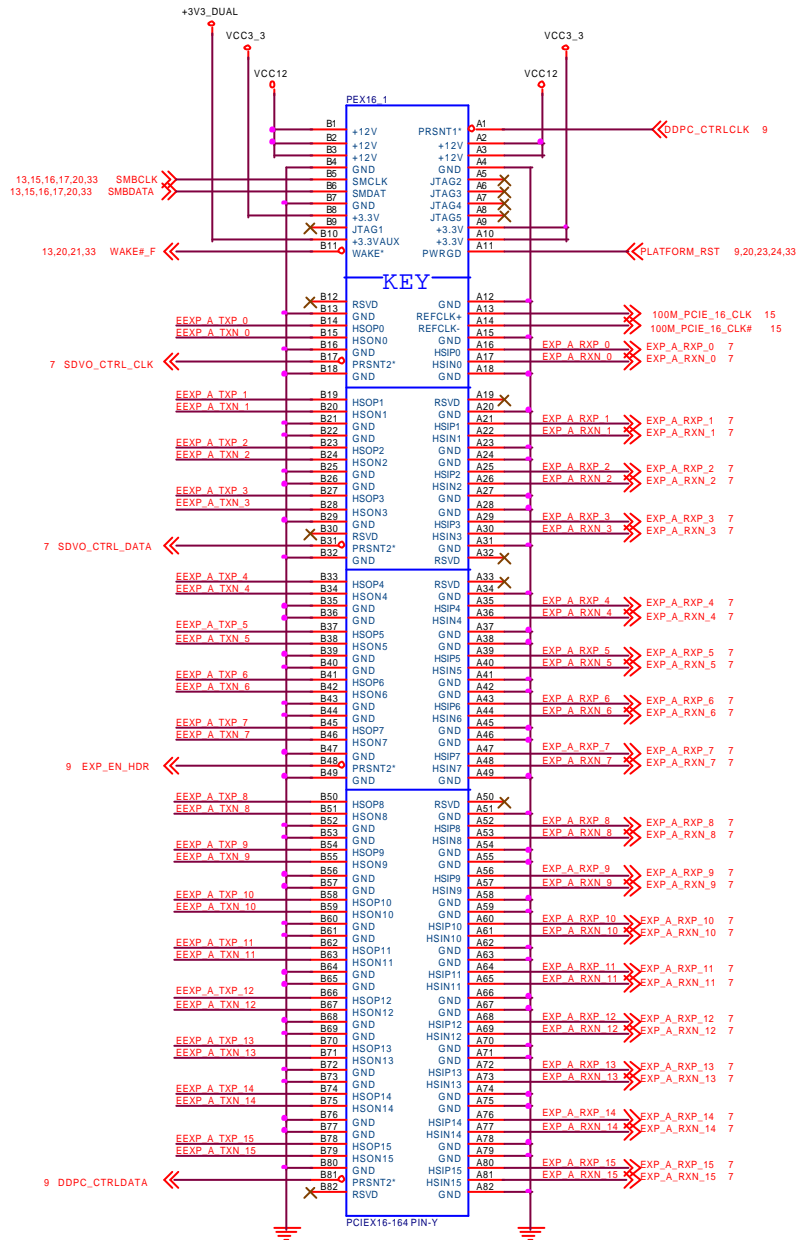
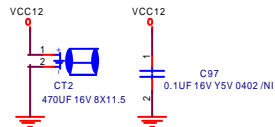
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CH-A

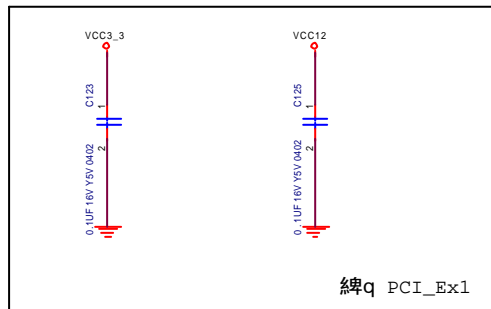
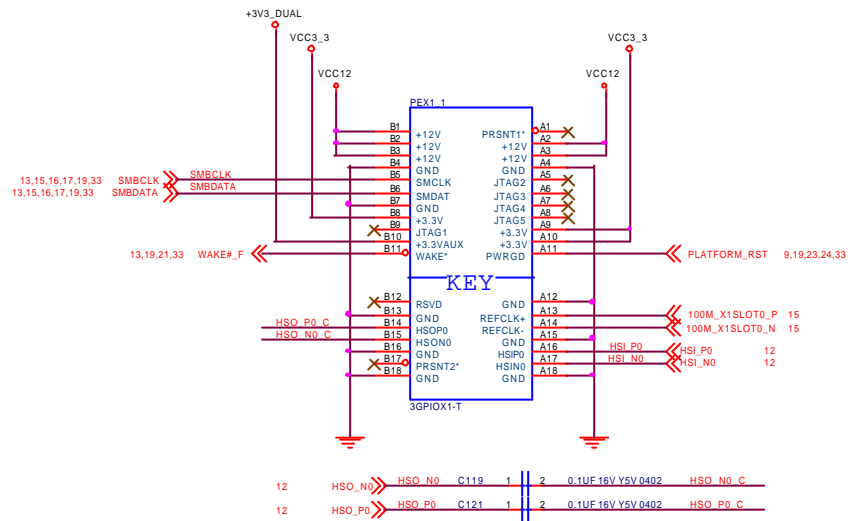


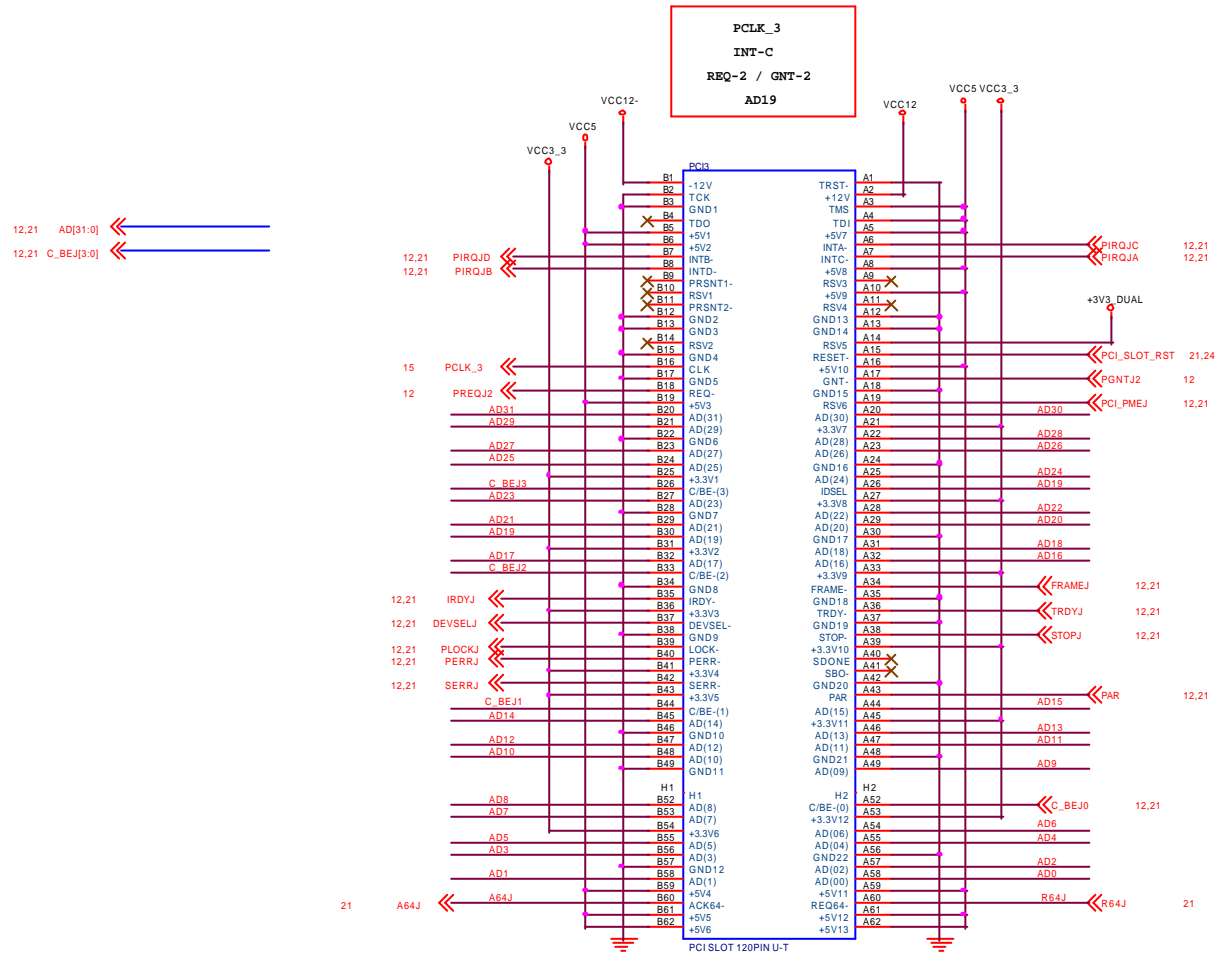
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CH-B

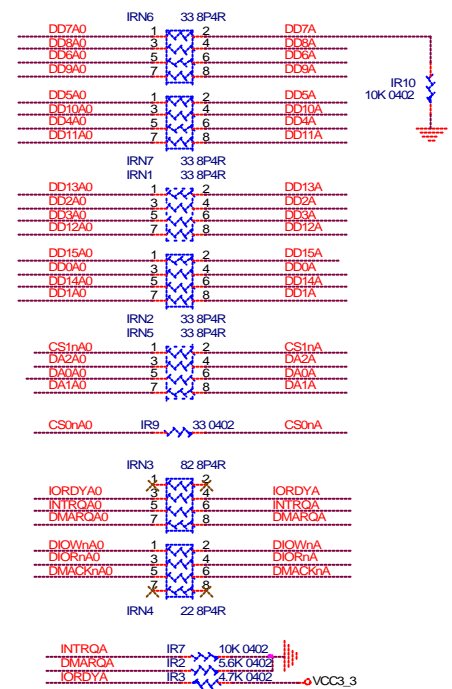
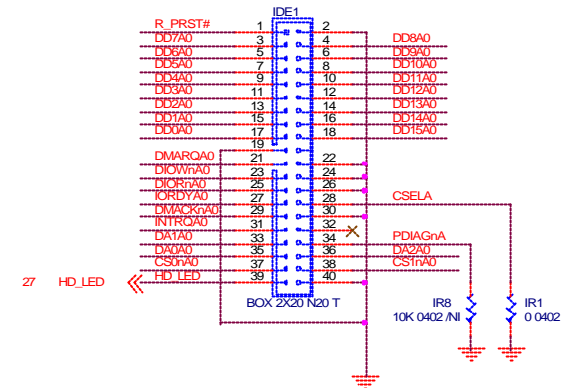
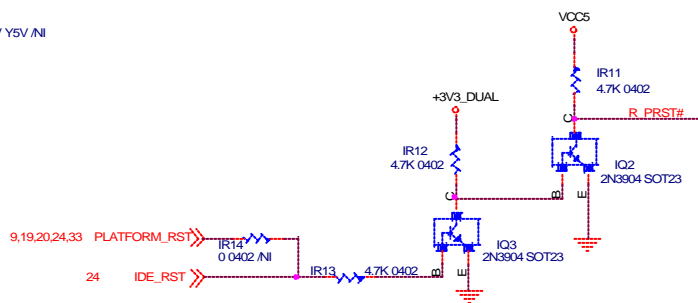
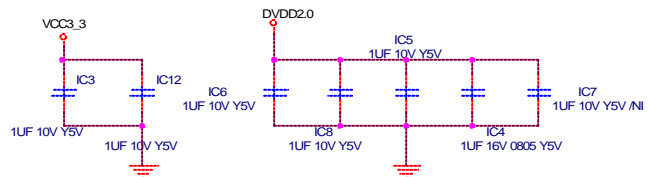
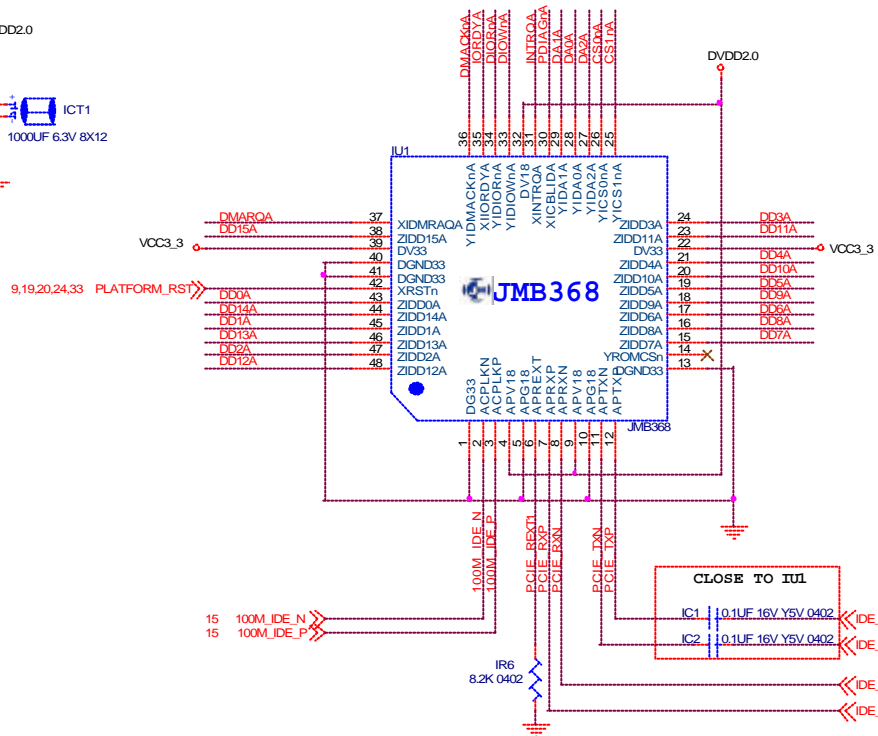
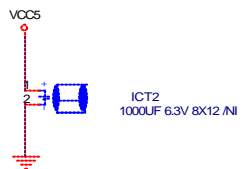
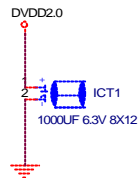
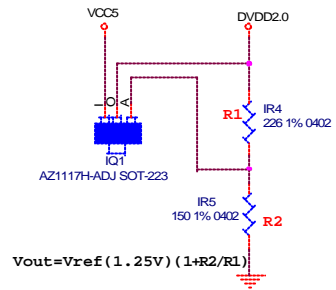
7 EXP_A_TXP_0	EXP_A_TXP_0	C65	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_0
7 EXP_A_TXN_0	EXP_A_TXN_0	C66	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_0
7 EXP_A_TXP_1	EXP_A_TXP_1	C67	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_1
7 EXP_A_TXN_1	EXP_A_TXN_1	C68	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_1
7 EXP_A_TXP_2	EXP_A_TXP_2	C69	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_2
7 EXP_A_TXN_2	EXP_A_TXN_2	C70	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_2
7 EXP_A_TXP_3	EXP_A_TXP_3	C71	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_3
7 EXP_A_TXN_3	EXP_A_TXN_3	C72	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_3
7 EXP_A_TXP_4	EXP_A_TXP_4	C73	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_4
7 EXP_A_TXN_4	EXP_A_TXN_4	C74	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_4
7 EXP_A_TXP_5	EXP_A_TXP_5	C75	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_5
7 EXP_A_TXN_5	EXP_A_TXN_5	C76	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_5
7 EXP_A_TXP_6	EXP_A_TXP_6	C77	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_6
7 EXP_A_TXN_6	EXP_A_TXN_6	C78	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_6
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7 EXP_A_TXN_7	EXP_A_TXN_7	C80	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_7
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7 EXP_A_TXN_9	EXP_A_TXN_9	C84	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_9
7 EXP_A_TXP_10	EXP_A_TXP_10	C85	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_10
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7 EXP_A_TXP_11	EXP_A_TXP_11	C87	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_11
7 EXP_A_TXN_11	EXP_A_TXN_11	C88	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_11
7 EXP_A_TXP_12	EXP_A_TXP_12	C89	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_12
7 EXP_A_TXN_12	EXP_A_TXN_12	C90	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_12
7 EXP_A_TXP_13	EXP_A_TXP_13	C91	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_13
7 EXP_A_TXN_13	EXP_A_TXN_13	C92	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_13
7 EXP_A_TXP_14	EXP_A_TXP_14	C93	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_14
7 EXP_A_TXN_14	EXP_A_TXN_14	C94	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXN_14
7 EXP_A_TXP_15	EXP_A_TXP_15	C95	1	2	0.1UF 16V Y5V 0402	EEXP_A_TXP_15
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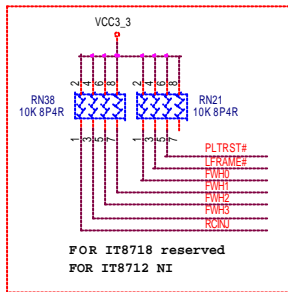
VCC3_3 R18 2.2K 0402/NI DDPC_CTRLDATA
VCC3_3 R35 2.2K 0402/NI DDPC_CTRLCLK



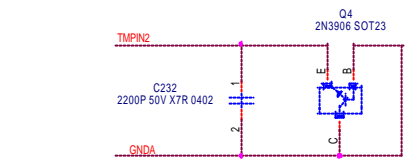
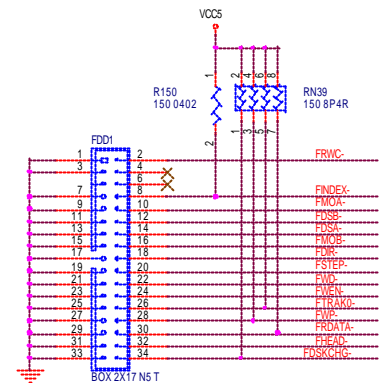
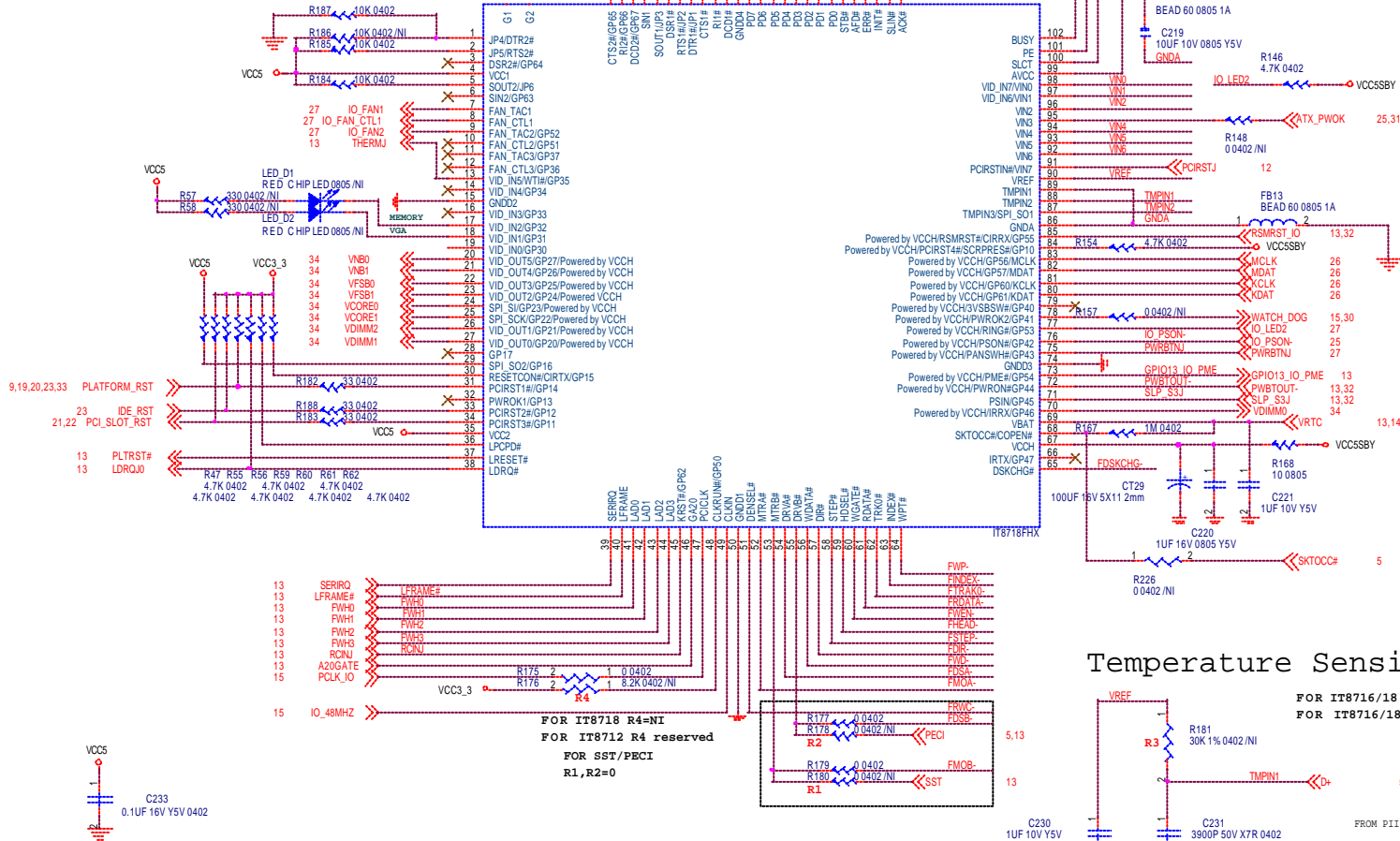




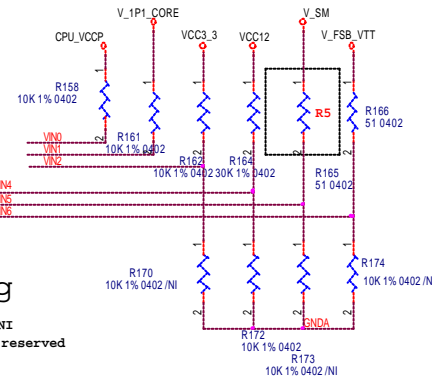




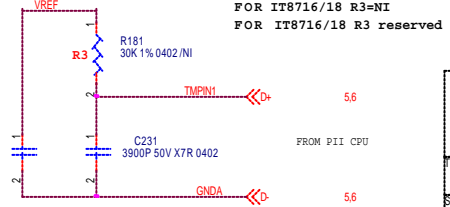
When pin1 JP4=Low,the GPIO pins status will run in Push Pull MODE



Voltage Sensing
FOR IT8718 R5=51
FOR IT8712 R5=10K



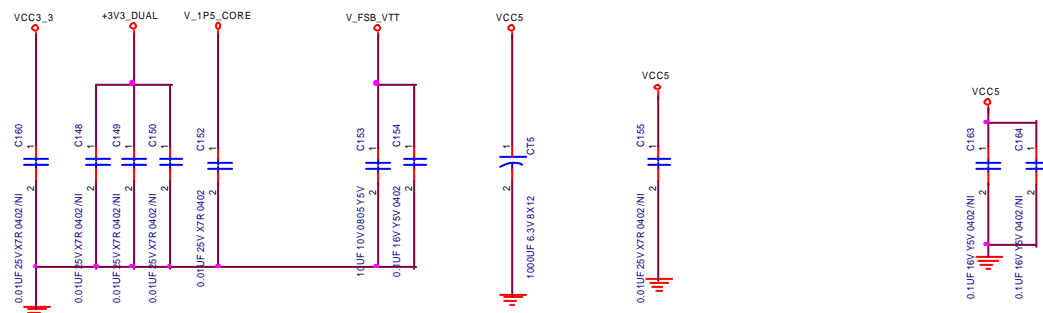
Temperature Sensing

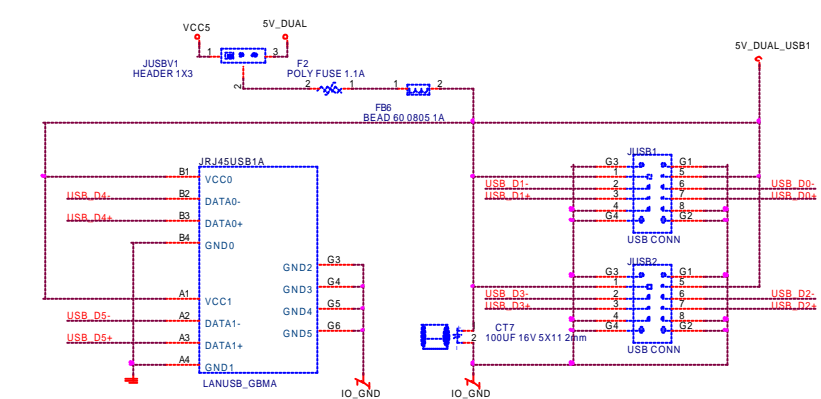
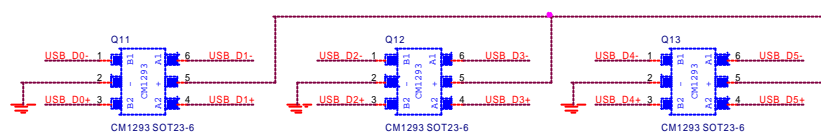
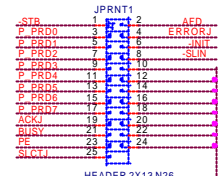


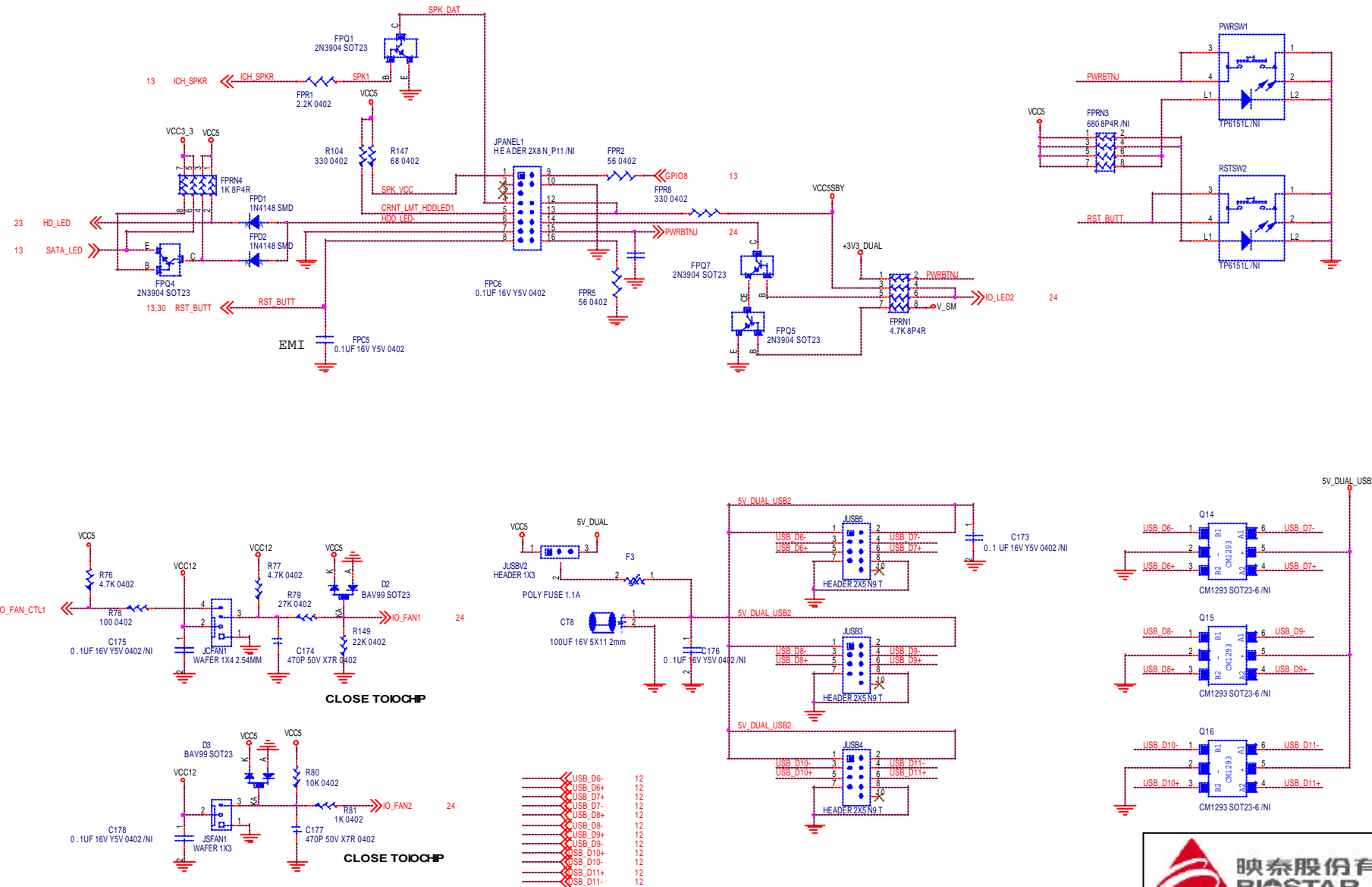
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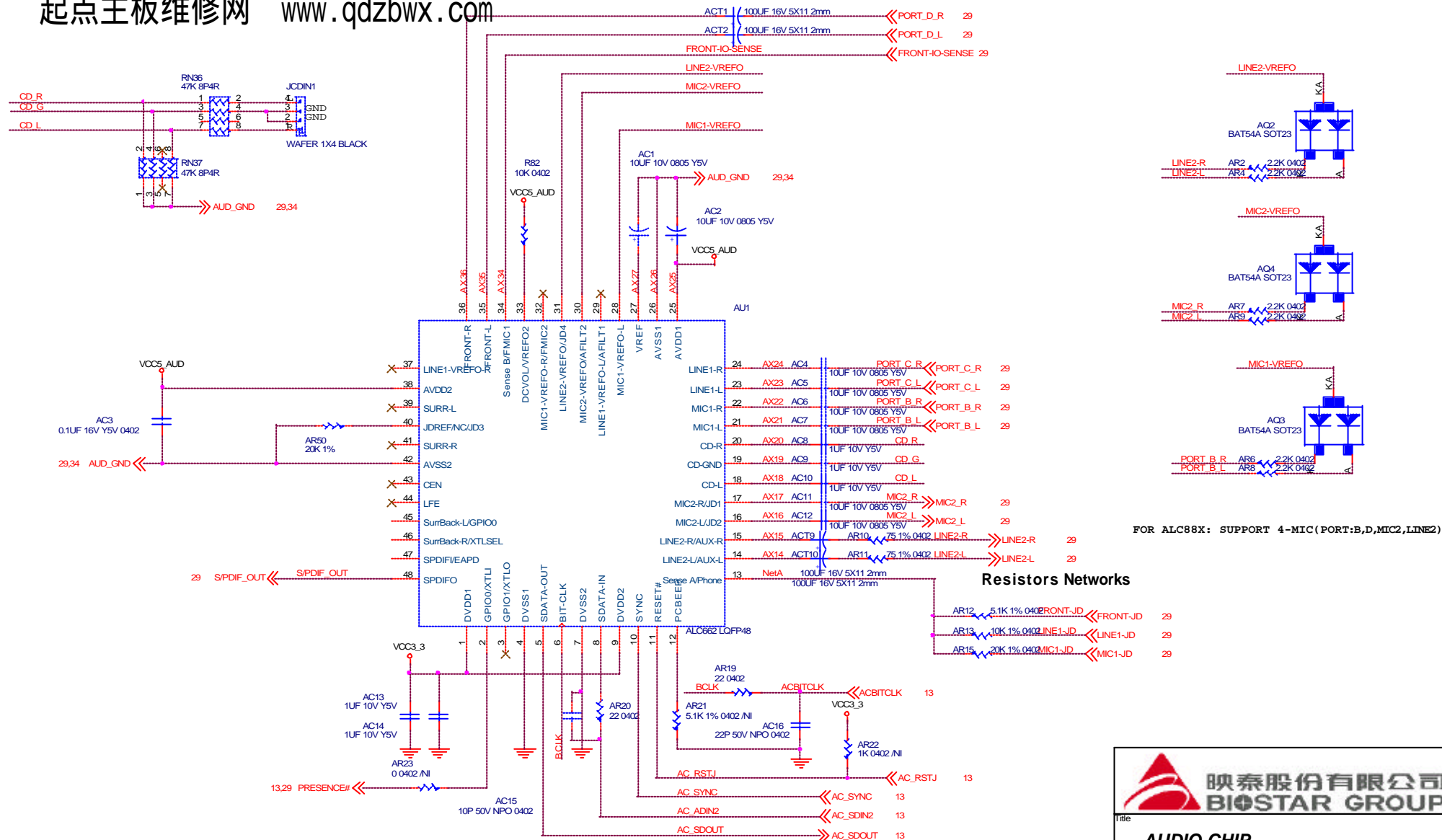
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Size: Document Number
Date: Tuesday, July 28, 2009
Sheet: 24 of 35

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Rev: 6.0

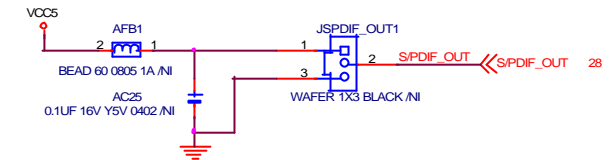
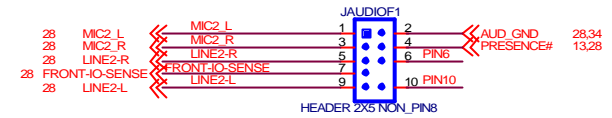
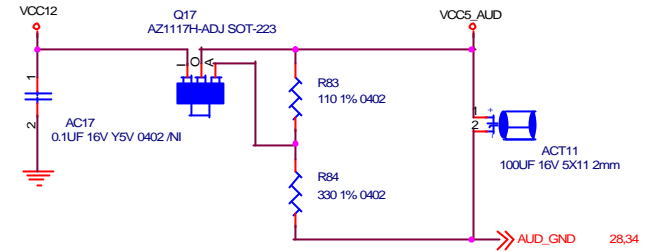
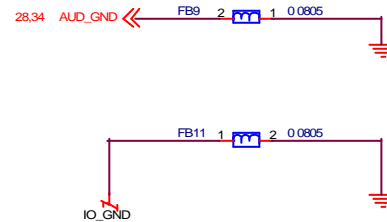
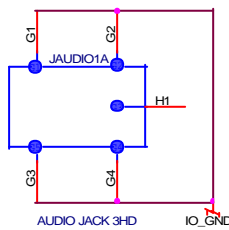
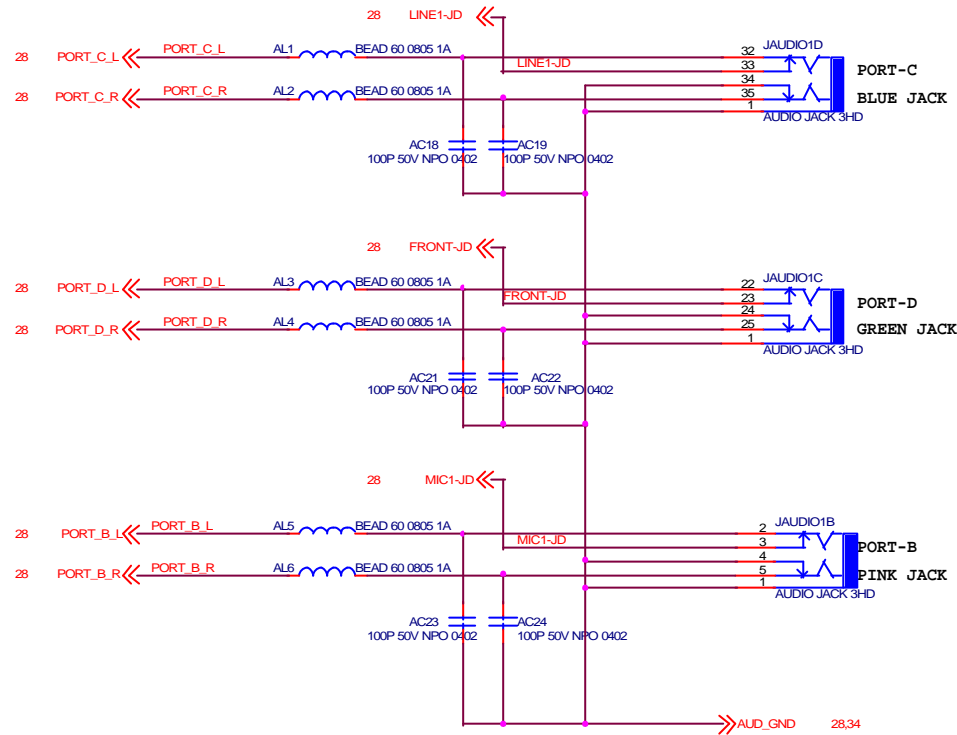






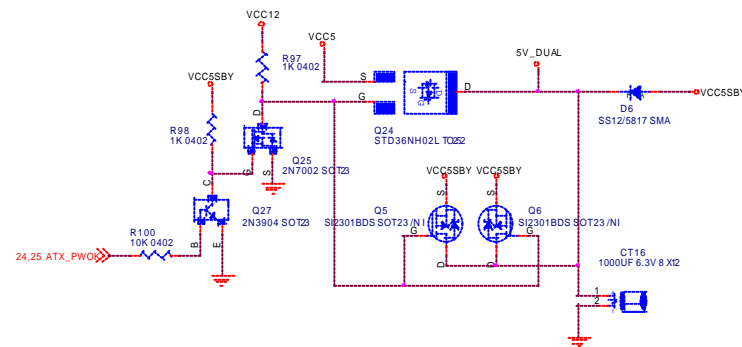
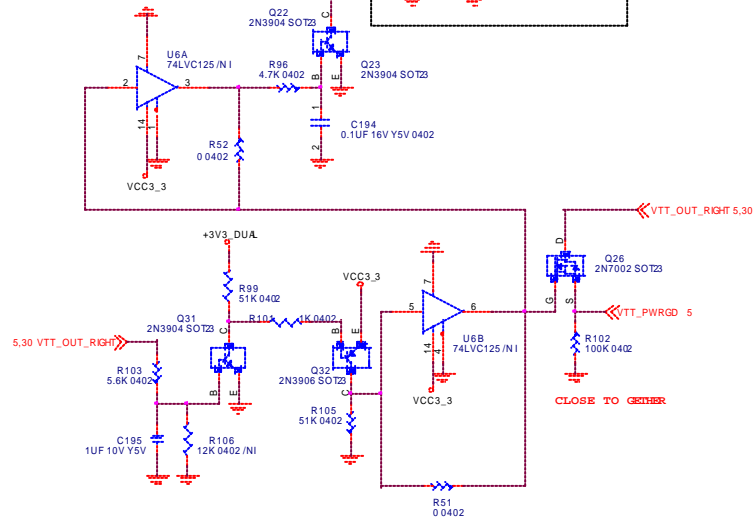
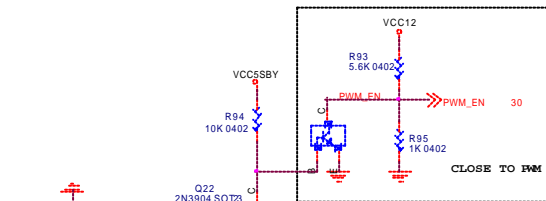
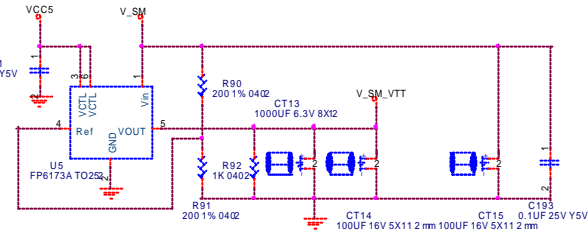
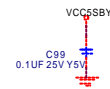
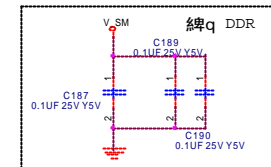
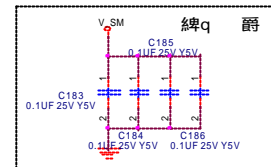
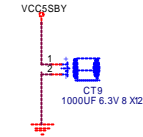
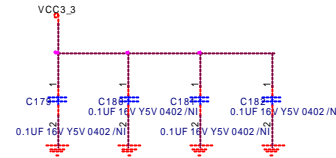
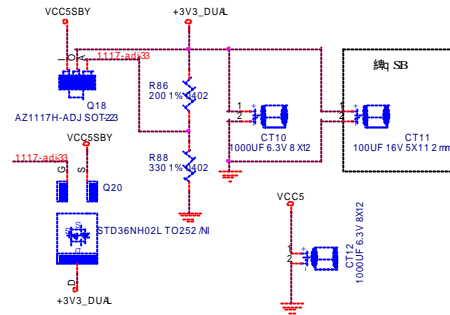
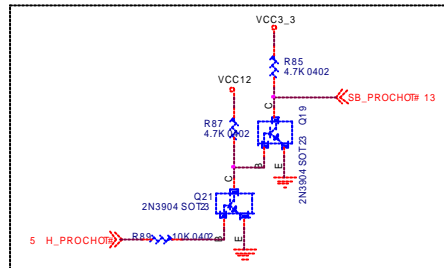


Rear Panel Onboard Analog I/O

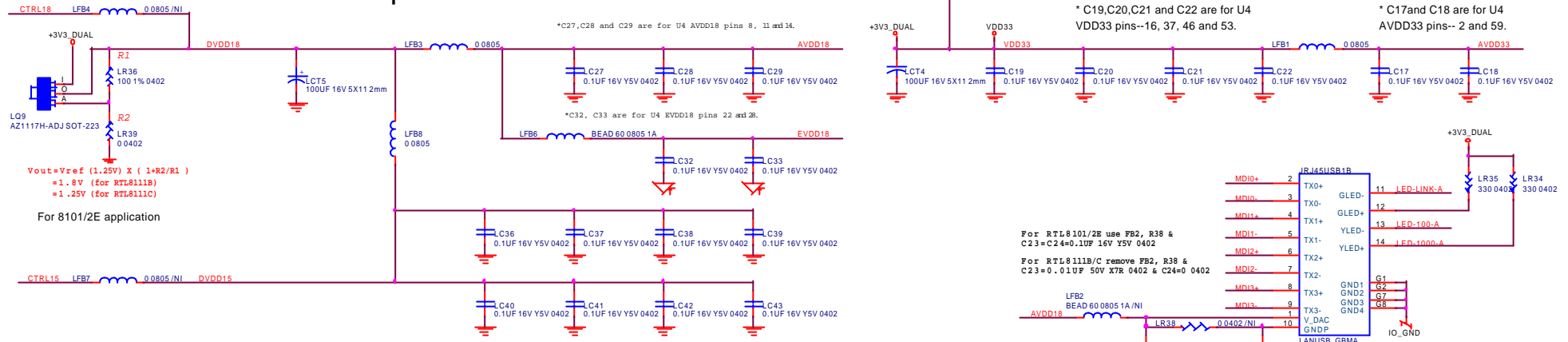


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Title			Rev
AUDIO CONNECTORS			
Size	Document Number	Rev	
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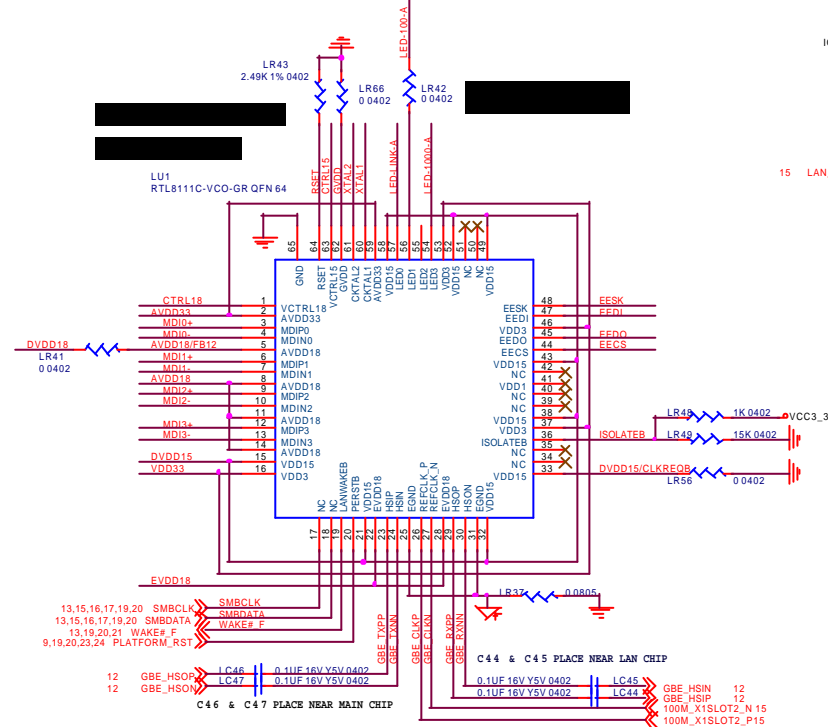


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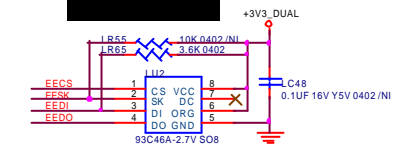
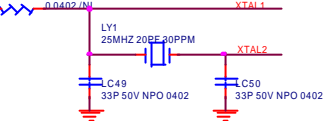
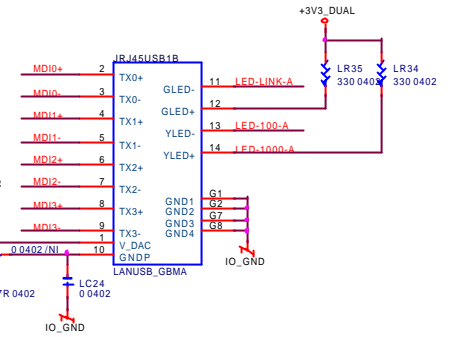


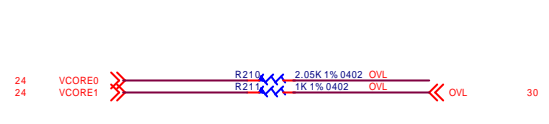
Remove FB4, FB7 for RTL8111C
Remove FB8, R36, R39, Q9 for RTL8101/2E

Power domain chart	RTL8111C/ RTL8102E
AVDD33	3.3V
AVDD18	1.2V
EVDD18	1.2V
DVDD15	1.2V



For RTL8101/2E use FB2, R38 & C23=C24=0.1UF 16V Y5V 0402
For RTL8111B/C remove FB2, R38 & C23=0.01UF 50V X7R 0402 & C24=0 0402

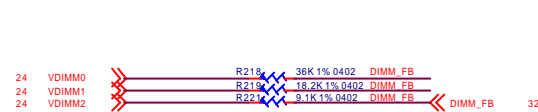




Vcore0	Vcore1	Voltage
0	0	+0.7V
1	0	+0.6V
0	1	+0.5V
1	1	+0.4V



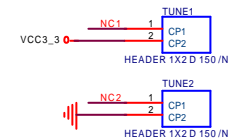
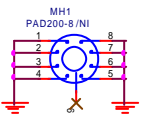
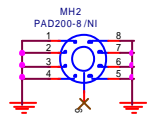
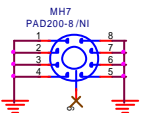
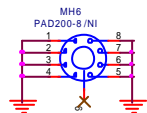
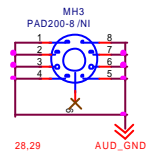
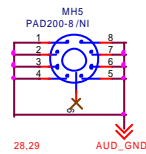
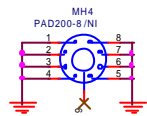
Vcore0	Vcore1	Voltage
0	0	1.2V
1	0	1.3V
0	1	1.4V
1	1	1.5V



VDIMM0	VDIMM1	VDIMM2	Voltage
0	0	0	+0.1V (1.925V)
1	0	0	+0.2V
0	1	0	+0.3V
1	1	0	+0.4V
0	0	1	+0.5V
1	0	1	+0.6V
0	1	1	+0.7V
1	1	1	+0.8V



VDIMM0	VDIMM1	Voltage
0	0	1.1V
1	0	1.2V
0	1	1.3V
1	1	1.4V





JKBV1(1_2)
JUMPER 2P R



JCMOS1(1_2)
JUMPER 2P B



JUSBV1(1_2)
JUMPER 2P R



(SY1)
XTAL WIRE



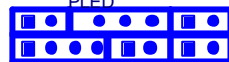
JUSBV2(1_2)
JUMPER 2P R



32.768 垂綺铭結

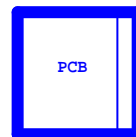
New JPANEL1 JPANEL1 2*8

JPANEL1(9_10) JPANEL1(15_16)
HEADER 1X2 HEADER 1X2
JPANEL1(11_14)
PLED



JPANEL1(1_4) JPANEL1(5_6) JPANEL1(7_8)
SPK HLED RST

PCB



IP43A-A7S V6.0

(PCB)



POLON 305x220

(BAT1)



3V BATTERY SONY

(U1)



SPI MX25L8005 DIP

(NB1)



NBHS-T

(SB1)



SBLS